Electrical Engineering

Degrees Offered

• Masters of Science, Electrical Engineering (M.S.E.E.)
• Doctor of Philosophy, Electrical Engineering (Ph.D.)

Program Description

The Masters of Science in Electrical Engineering (M.S.E.E.) degree program is intended for students who have an undergraduate degree in Electrical Engineering, Computer Engineering, or a closely related discipline, and wish to broaden their depth of understanding in one or more areas of the field. Program graduates will be qualified to pursue careers in industry, government, or further academic study. The Doctor of Philosophy program should be considered by those with superior academic achievement and who desire to pursue a career of research or teaching.

Masters Program Educational Objectives & Outcomes

The objective of the Master of Science in Electrical Engineering (M.S.E.E.) degree program is to produce graduates who have the knowledge, skills, and attitudes that will ensure success in professional positions in business, industry, research, government service, or in further graduate or professional study.

Specific outcomes that will be achieved by graduates of the program are:

1. Achieve a depth of proficiency in a specific field of Electrical Engineering by completing major courses in one of four areas: electronics and photonics; systems and signals; computer systems; or software and knowledge engineering.
2. Achieve a breadth of understanding of Electrical Engineering by completing minor coursework requirements in another area, and by participation in graduate seminar requirements.
3. Demonstrate professionalism and communication skills through completion of coursework, project or thesis defense.

Doctoral Program Educational Objectives & Outcomes

The objective of the Ph.D. Program in Electrical Engineering degree program is to produce graduates who have the knowledge, skills, and attitudes that will ensure success in professional positions in business, industry, research, government service, or in further graduate or professional study.

Specific outcomes that will be achieved by graduates of the program are:

1. Achieve a depth of understanding in Electrical Engineering, as demonstrated by completion of core Ph.D. courses and examination on that material through the Qualifying Examination process.
2. Achieve a breadth of understanding of the Electrical Engineering discipline, as demonstrated by completion of remaining doctoral coursework and participation in graduate seminar.
3. Demonstrate the ability to conduct independent research by completion and defense of a dissertation.

Masters Admissions

Students admitted into a program are designated as regular, provisional, or non-degree status. Regular status is given to students who are granted unconditional admissions. Provisional status is given to students who have deficiencies to make up such as incomplete credentials or other reasons as identified by the graduate coordinator. In all cases, the student’s letter of admission will state what must be done to attain regular status, and students must sign and date this letter no later than the first registration. Non-degree status is granted case-by-case by the graduate coordinator. Basically, a non-degree student is one who may take courses but sometimes with no plan of study or any guarantee for attaining provisional status.

Doctoral Admissions

As a first step, students must satisfy provisions under the “Admission Requirements for All Programs” and must submit a statement of purpose. Students who hold an M.S.E.E. or M.S.E. (or equivalent) degree will be considered for admission with regular status into the Ph.D. program. Students who hold a master’s degree in the sciences or engineering, excluding M.S.E.E. or M.S.E., will be considered for admission with provisional status and will likely have coursework deficiencies to remove. All other students must apply for admission into a master’s program as the first stage in attaining the Ph.D.

REMOVING DEFICIENCIES

Prior to the first week of classes, new Ph.D. students must meet with the graduate coordinator to select classes. This interview determines if the student needs remedial work in order to pursue a graduate degree. Students with deficiencies may be required to take courses as prerequisites for graduate courses. Deficiencies are usually noted as a condition for admission. However, they may also be specified during the interview or later.
During the second semester, students must form their Advisory and Examining Committee (AEC) and write a plan of study. The AEC may also identify additional deficiencies to be removed, but this is rare since deficiencies should have been identified earlier in the student’s career.

Curriculum in Master of Science in Electrical Engineering Masters

A candidate for the M.S. degree in electrical engineering must comply with the rules and regulations as outlined in the WVU Graduate Catalog and the specific requirements of the Statler College and the Lane Department of Computer Science and Electrical Engineering.

Program Requirements

All M.S. degree candidates are required to perform research and follow a planned program of study. The student’s research advisor, in conjunction with the student’s Advising and Examining Committee (AEC) will be responsible for determining the plan of study appropriate to the student’s needs. The underlying principle of the planned program is to provide the students with the necessary support to complete their degree and prepare them for their career.

Curriculum Requirements

A minimum cumulative GPA of 3.0 is required

Course Requirements

A minimum of 60% of courses must be from 500 level or above

<table>
<thead>
<tr>
<th>Course</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>EE 796 Graduate Seminar</td>
<td>1</td>
</tr>
</tbody>
</table>

Area of Concentration

Complete one Area of Concentration as follows: **

<table>
<thead>
<tr>
<th>Course</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>One Core course</td>
<td>9</td>
</tr>
<tr>
<td>Two Elective courses</td>
<td></td>
</tr>
</tbody>
</table>

Complete two additional Areas of Concentration as follows:

<table>
<thead>
<tr>
<th>Course</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>One Core Course</td>
<td>6</td>
</tr>
<tr>
<td>Elective courses</td>
<td>9</td>
</tr>
</tbody>
</table>

Choose three of the following:

- Any BIOM, CE, CHE, CHEM, CPE, CS, EE, IENG, IH&S, MAE, MATH, MINE, PNGE, PHYS, SAFM, SENG, or STAT courses 400-799

Complete 1 of the following options: 7-8

**Thesis Option - 7 hours**

<table>
<thead>
<tr>
<th>Course</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>EE 796 Graduate Seminar (1 hour)</td>
<td></td>
</tr>
<tr>
<td>EE 697 Research (6 hours)</td>
<td></td>
</tr>
<tr>
<td>Written Research Proposal</td>
<td></td>
</tr>
<tr>
<td>Thesis</td>
<td></td>
</tr>
<tr>
<td>Final Oral or Written Examination</td>
<td></td>
</tr>
</tbody>
</table>

**Problem Report Option - 8 hours**

<table>
<thead>
<tr>
<th>Course</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>Complete 5 additional hours of coursework</td>
<td></td>
</tr>
<tr>
<td>EE 697 Research (3 hours)</td>
<td></td>
</tr>
<tr>
<td>Written Research Proposal</td>
<td></td>
</tr>
<tr>
<td>Formal written report or professional report/paper</td>
<td></td>
</tr>
<tr>
<td>Final Oral or Written Examination</td>
<td></td>
</tr>
</tbody>
</table>

**Coursework Option - 8 hours** ***

<table>
<thead>
<tr>
<th>Course</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>Complete 8 additional hours of coursework</td>
<td></td>
</tr>
<tr>
<td>Final Oral or Written Examination</td>
<td></td>
</tr>
</tbody>
</table>

Total Hours 32-33

* Students who do not hold a baccalaureate degree in electrical engineering are required to take a set of undergraduate electrical engineering courses above and beyond the minimum coursework requirements.

** The Theory of Computing Area of Concentration may not be used to fulfill this requirement.

*** This option is open only to professionals employed full-time in local industry.

Areas of Concentration

**ELECTRONIC AND PHOTONICS**

Core Courses
### Electrical Engineering

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>EE 550</td>
<td>Advanced Semiconductor Electronics</td>
<td>3</td>
</tr>
<tr>
<td>EE 551</td>
<td>Linear Integrated Circuits</td>
<td>3</td>
</tr>
<tr>
<td><strong>Elective Courses</strong></td>
<td><strong>3</strong></td>
<td></td>
</tr>
<tr>
<td>EE 435</td>
<td>Introduction to Power Electronics</td>
<td>3</td>
</tr>
<tr>
<td>EE 437</td>
<td>Fiber Optics Communications</td>
<td>3</td>
</tr>
<tr>
<td>EE 455</td>
<td>Introduction to Microfabrication</td>
<td>3</td>
</tr>
<tr>
<td>EE 457</td>
<td>Fundamentals of Photonics</td>
<td>3</td>
</tr>
<tr>
<td>or EE 591</td>
<td>Advanced Topics</td>
<td>3</td>
</tr>
<tr>
<td>EE 528</td>
<td>Biomedical Microdevices</td>
<td>3</td>
</tr>
<tr>
<td>EE 591L</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>EE 650</td>
<td>Optoelectronics</td>
<td>3</td>
</tr>
<tr>
<td>EE 694</td>
<td>Optoelectronics</td>
<td>3</td>
</tr>
<tr>
<td>PHYS 771</td>
<td>Introduction to Solid State Physics</td>
<td>3</td>
</tr>
<tr>
<td>PHYS 772</td>
<td>Semiconductor Physics</td>
<td>3</td>
</tr>
<tr>
<td>PHYS 773</td>
<td>Collective Phenomena in Solids</td>
<td>3</td>
</tr>
<tr>
<td>CHE 466</td>
<td>Electronic Materials Processing</td>
<td>3</td>
</tr>
<tr>
<td>BIOL 493</td>
<td>Special Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>or BIOL 593</td>
<td>Special Topics</td>
<td><strong>3</strong></td>
</tr>
</tbody>
</table>

### SIGNALS AND SYSTEMS

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
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</thead>
<tbody>
<tr>
<td>EE 513</td>
<td>Stochastic Systems Theory</td>
<td>3</td>
</tr>
<tr>
<td>EE 515</td>
<td>Linear Control Systems</td>
<td>3</td>
</tr>
<tr>
<td><strong>Elective Courses</strong></td>
<td><strong>3</strong></td>
<td></td>
</tr>
<tr>
<td>EE 461</td>
<td>Introduction to Communications Systems</td>
<td>3</td>
</tr>
<tr>
<td>EE 465</td>
<td>Introduction to Digital Image Processing</td>
<td>3</td>
</tr>
<tr>
<td>EE 517</td>
<td>Optimal Control</td>
<td>3</td>
</tr>
<tr>
<td>EE 519</td>
<td>Digital Control</td>
<td>3</td>
</tr>
<tr>
<td>EE 531</td>
<td>Advanced Electrical Machinery</td>
<td>3</td>
</tr>
<tr>
<td>EE 533</td>
<td>Computer Applications in Power System Analysis</td>
<td>3</td>
</tr>
<tr>
<td>EE 535</td>
<td>Power System Control and Stability</td>
<td>3</td>
</tr>
<tr>
<td>EE 561</td>
<td>Communication Theory</td>
<td>3</td>
</tr>
<tr>
<td>EE 562</td>
<td>Wireless Communication System</td>
<td>3</td>
</tr>
<tr>
<td>EE 565</td>
<td>Advanced Image Processing</td>
<td>3</td>
</tr>
<tr>
<td>EE 567</td>
<td>Coding Theory</td>
<td>3</td>
</tr>
<tr>
<td>EE 568</td>
<td>Information Theory</td>
<td>3</td>
</tr>
<tr>
<td>EE 569</td>
<td>Digital Video Processing</td>
<td>3</td>
</tr>
<tr>
<td>EE 613</td>
<td>Detection and Estimation Theory</td>
<td>3</td>
</tr>
<tr>
<td>EE 625</td>
<td>Advanced Signal Processing</td>
<td>3</td>
</tr>
<tr>
<td>EE 713</td>
<td>Large-Scale System Modeling</td>
<td>3</td>
</tr>
<tr>
<td>EE 731</td>
<td>Real Time Control of Power System</td>
<td>3</td>
</tr>
<tr>
<td>EE 733</td>
<td>Protection of Power Systems</td>
<td>3</td>
</tr>
</tbody>
</table>

### COMPUTER SYSTEMS

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPE 670</td>
<td>Switching Circuit Theory 1</td>
<td>3</td>
</tr>
<tr>
<td>CS 550</td>
<td>Theory of Operating Systems</td>
<td>3</td>
</tr>
<tr>
<td><strong>Elective Courses</strong></td>
<td><strong>3</strong></td>
<td></td>
</tr>
<tr>
<td>CPE 435</td>
<td>Computer Incident Response</td>
<td>3</td>
</tr>
<tr>
<td>CPE 520</td>
<td>Application of Neural Networks</td>
<td>3</td>
</tr>
<tr>
<td>CPE 521</td>
<td>Applied Fuzzy Logic</td>
<td>3</td>
</tr>
<tr>
<td>CPE 536</td>
<td>Computer Data Forensics</td>
<td>3</td>
</tr>
<tr>
<td>Course</td>
<td>Title</td>
<td>Credits</td>
</tr>
<tr>
<td>--------</td>
<td>--------------------------------------------</td>
<td>---------</td>
</tr>
<tr>
<td>CPE 538</td>
<td>Intro Computer Security Management</td>
<td>3</td>
</tr>
<tr>
<td>CS 533</td>
<td>Developing Portable Software</td>
<td>3</td>
</tr>
<tr>
<td>CS 453</td>
<td>Data and Computer Communications</td>
<td>3</td>
</tr>
<tr>
<td>CS 539</td>
<td>Computer Forensics and the Law</td>
<td>3</td>
</tr>
<tr>
<td>CS 555</td>
<td>Advanced Computer Systems Architecture</td>
<td>3</td>
</tr>
<tr>
<td>CS 556</td>
<td>Distributed and Pervasive Compt</td>
<td>3</td>
</tr>
<tr>
<td>CS 568</td>
<td>Computer Network Forensics</td>
<td>3</td>
</tr>
<tr>
<td>CS 570</td>
<td>Interactive Computer Graphics</td>
<td>3</td>
</tr>
<tr>
<td>CS 578</td>
<td>Medical Image Analysis</td>
<td>3</td>
</tr>
<tr>
<td>EE 591V</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
</tbody>
</table>

**SOFTWARE/KNOWLEDGE ENGINEERING**

**Core Courses**

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPE 684</td>
<td>Advanced Real-Time Systems</td>
<td>3</td>
</tr>
<tr>
<td>CS 573</td>
<td>Advanced Data Mining</td>
<td>3</td>
</tr>
<tr>
<td>CS 677</td>
<td>Pattern Recognition</td>
<td>3</td>
</tr>
<tr>
<td>CS 630</td>
<td>Empirical Methods in Software Engineering and Computer Science</td>
<td>3</td>
</tr>
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</table>

**Elective Courses**

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>BIOM 693</td>
<td>Special Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>CS 533</td>
<td>Developing Portable Software</td>
<td>3</td>
</tr>
<tr>
<td>CS 558</td>
<td>Multimedia Systems</td>
<td>3</td>
</tr>
<tr>
<td>CS 572</td>
<td>Advanced Artificial Intelligence Techniques</td>
<td>3</td>
</tr>
<tr>
<td>CS 578</td>
<td>Medical Image Analysis</td>
<td>3</td>
</tr>
<tr>
<td>or CS 778</td>
<td>Medical Image Analysis</td>
<td>3</td>
</tr>
<tr>
<td>CS 665</td>
<td>Computer System Security</td>
<td>3</td>
</tr>
<tr>
<td>CS 736</td>
<td>Software Performance Engineering</td>
<td>3</td>
</tr>
<tr>
<td>CS 791X</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>CS 757</td>
<td>Distributed Systems and Algorithms</td>
<td>3</td>
</tr>
<tr>
<td>CS 691X</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>EE 565</td>
<td>Advanced Image Processing</td>
<td>3</td>
</tr>
<tr>
<td>SENG 550</td>
<td>Object Oriented Design</td>
<td>3</td>
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</table>

**THEORY OF COMPUTING**

**Core Courses**

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
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<tbody>
<tr>
<td>CS 510</td>
<td>Formal Specification of Language</td>
<td>3</td>
</tr>
<tr>
<td>CS 520</td>
<td>Advanced Analysis of Algorithms</td>
<td>3</td>
</tr>
<tr>
<td>CS 525</td>
<td>Computational Complexity</td>
<td>3</td>
</tr>
</tbody>
</table>

**Elective Courses**

<table>
<thead>
<tr>
<th>Course</th>
<th>Title</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>CS 410</td>
<td>Compiler Construction</td>
<td>3</td>
</tr>
<tr>
<td>CS 420</td>
<td>Design of Algorithms</td>
<td>3</td>
</tr>
<tr>
<td>CS 422</td>
<td>Automata Theory</td>
<td>3</td>
</tr>
<tr>
<td>CS 426</td>
<td>Discrete Mathematics 2</td>
<td>3</td>
</tr>
<tr>
<td>CS 512</td>
<td></td>
<td></td>
</tr>
<tr>
<td>CS 522</td>
<td></td>
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<td>CS 722</td>
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<tr>
<td>CS 725</td>
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<tr>
<td>CS 727</td>
<td>Information Dissemination</td>
<td>3</td>
</tr>
<tr>
<td>CS 791X</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>CS 591B</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>CS 791E</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
<tr>
<td>CS 591Q</td>
<td>Advanced Topics</td>
<td>1-6</td>
</tr>
</tbody>
</table>
**Final Examination**

M.S. students following the thesis or problem report option must prepare a written research proposal. The proposal must be approved by the student’s AEC at least one semester prior to the final oral examination.

All students, regardless of option, are required to pass a final oral or written examination, administered by their AEC, covering the thesis or problem report and/or related course material.

All master’s students must defend their thesis or problem report at an oral exam, attended by all members of the committee.

A student who fails the research defense may repeat the defense at most once, at a time determined by the AEC but not necessarily during the same semester.

**Suggested Plan of Study**

The plan below illustrates the Thesis Option. It is important for students to take courses in the order specified as much as possible; all prerequisites and concurrent requirements must be observed. A typical M.S.E.E degree program that completes degree requirements in one and half years is as follows. Those students who lack course prerequisites may require more than three semesters of full-time study to complete the degree. Students with research assistantships may also require more than three semesters to complete the degree.

<table>
<thead>
<tr>
<th>First Year</th>
<th>Hours</th>
<th>Spring</th>
<th>Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>Field of Study 1 Core Course</td>
<td>3</td>
<td>Field of Study 1 Elective Course</td>
<td>3</td>
</tr>
<tr>
<td>Field of Study 1 Elective Course</td>
<td>3</td>
<td>Field of Study 2 Core Course</td>
<td>3</td>
</tr>
<tr>
<td>Elective Course</td>
<td>3</td>
<td>Elective Course</td>
<td>3</td>
</tr>
<tr>
<td>EE 796</td>
<td>1</td>
<td>EE 796</td>
<td>1</td>
</tr>
<tr>
<td><strong>Total credit hours:</strong> 10</td>
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<td><strong>Total credit hours:</strong> 10</td>
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</table>

<table>
<thead>
<tr>
<th>Second Year</th>
<th>Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>Field of Study 3 Core Course</td>
<td>3</td>
</tr>
<tr>
<td>Elective Course</td>
<td>3</td>
</tr>
<tr>
<td>EE 697</td>
<td>6</td>
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<tr>
<td><strong>Total credit hours:</strong> 12</td>
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**Curriculum in Doctor of Philosophy – Electrical Engineering Requirements**

A candidate for the Ph.D. degree with a major in electrical engineering must comply with the rules and regulations as outlined in the WVU Graduate Catalog and the specific requirements of the Statler College and the Lane Department of Computer Science and Electrical Engineering.

**Program Requirements**

The doctor of philosophy degree with a major in electrical engineering is administered through the college’s interdisciplinary Ph.D. program. The research work for the doctoral dissertation must show a high degree of originality on the part of the student and must constitute an original contribution to the art and science of electrical engineering.

All Ph.D. degree candidates are required to perform research and follow a planned program of study. The student’s research advisor, in conjunction with the student’s Advising and Examining Committee (AEC) will be responsible for determining the plan of study appropriate to the student’s needs. The underlying principle of the planned program is to provide the students with the necessary support to complete their degree and prepare them for their career.

Research work for the doctoral dissertation must represent a significant contribution to engineering or computer science. It may entail a fundamental investigation into a specialized area. A minimum of twenty-four credit hours of research (EE 797 [http://catalog.wvu.edu/graduate/collegeofengineeringandmineralresources/thelanedepartmentofcomputerscienceandelectricalengineering/computer_engineering] is required.

**Curriculum Requirements**

A minimum cumulative GPA of 3.0 is required

**Course Requirements**

A minimum of six credit hours of 600 or higher level courses
A maximum of six credit hours may be in directed study (EE 795)

**Research**
Students who do not hold a baccalaureate degree in electrical engineering are required to take a set of undergraduate electrical engineering courses above and beyond the minimum coursework requirements.

Doctoral students who do not have an M.S.E.E. degree must either earn this degree, or complete coursework as required for the Master's degree with thesis option. It is not necessary to actually write a thesis. A minimum of twenty-four hours of coursework is required. Up to twelve hours may be transferred from work done at another institution.

A minimum of forty-two hours of coursework and thirty hours of independent research beyond a bachelor’s degree, or eighteen hours of coursework and twenty-four hours of independent research beyond an M.S. degree are required.

**Examinations**

**QUALIFYING EXAM**

All students must take and pass a written qualifying examination. Normally, the qualifying examination is given no later than one semester after completion of eighteen credit hours toward the doctoral degree. This examination is designed to assess the basic competency of students in the electrical engineering field to determine whether or not they have sufficient knowledge to undertake independent research.

The Lane Department of Computer Science and Electrical Engineering is organized in the following five Areas of Concentration. All Ph.D. degree programs use these Areas to provide organizational structure to the educational process as delineated under specific Ph.D. requirements. The significance of these Areas will be of particular importance in preparation for the Qualifying Exam as each area has designated Ph.D. Qualifier Core Courses as follows:

1. **Electronics and Photonics Area**
   - EE 550 Advanced Semiconductor Electronics
   - EE 551 Linear Integrated Circuits
   - EE 650 Optoelectronics

2. **Signals and Systems Area**
   - EE 513 Stochastic Systems Theory
   - EE 515 Linear Control Systems
   - EE 533 Computer Applications in Power System Analysis

3. **Computer Systems**
   - CPE 670 Switching Circuit Theory 1
   - CS 550 Theory of Operating Systems

4. **Software/Knowledge Engineering**
   - CPE 684 Advanced Real-Time Systems
   - CS 573 Advanced Data Mining
   - CS 591Q Pattern Recognition

5. **Theory of Computing**
   - CS 510 Formal Specification of Language
   - CS 520 Advanced Analysis of Algorithms
   - CS 525 Computational Complexity

Ph.D. students must make the first attempt to pass the qualifying exam within fourteen months of their enrollment if they already have a M.S. degree from the Lane Department of CSEE or within twenty-six months otherwise. The Ph.D. qualifying process consists of completion of a research project and oral examination. The project is intended to demonstrate the student's ability to assemble and analyze the relevant literature for a given research problem and to make preliminary steps towards his/her own contribution.

The oral exam will include:

1. Presentation by the student of his/her research project
2. Questions about the work, its context, and relevant literature
3. Questions about course work, focusing specifically on the three core courses for which the student has earned credit

The possible outcomes of the first year exam are: “Pass” which means the student is qualified to begin work towards the candidacy exam; “Pass with Recommended Coursework” which means the student is qualified to begin work towards a candidacy exam but certain courses must be taken; or “Fail”. Any student failing the qualifying exam on the initial attempt will have one additional attempt within six months. Failure of the exam on the second attempt will disqualify the student from further doctoral studies in the LCSEE program.

CANDIDACY EXAMINATION

In order to be admitted to candidacy, the student must pass a candidacy exam, which is designed to evaluate the student’s overall ability to engage in high-level research.

When all requirements are completed, the qualifying and candidacy examinations are passed, and the research proposal is successfully defended, the student is formally admitted to candidacy for the Ph.D. degree. For full-time students, admission to candidacy must occur within three years of entering the Ph.D. program.

FINAL EXAMINATION

At the completion of the dissertation research, candidates must prepare a dissertation and pass the final oral examination (defense) administered by their AEC.

In order to complete the Ph.D. requirements, a student must pass a final oral examination on the results embodied in the dissertation. This examination is open to the public and, in order to evaluate critically the student's competency, may include testing on material in related fields, as deemed necessary by the AEC. All requirements for the degree must be completed within five years after the student has been admitted to candidacy.

Suggested Plan of Study

It is important for students to take courses in the order specified as much as possible; all prerequisites and concurrent requirements must be observed. A typical doctoral degree program that completes degree requirements in three years is as follows. A typical Ph.D. program requires four to five years beyond the baccalaureate degree, although scholarly achievements are more important than length of program.

First Year

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<th>Fall</th>
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<td>EE 797</td>
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Second Year

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Third Year

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<td>EE 797</td>
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Total credit hours: 54

Major Learning Goals

ELECTRICAL ENGINEERING

It is our goal that in the first five years after graduation our students will:

(1) achieve success and proficiency in the Electrical Engineering profession,

(2) be recognized as leaders,

(3) and contribute to the well-being of society.